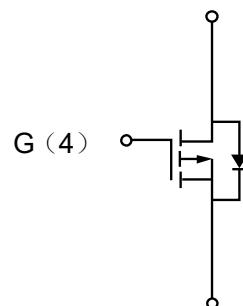


## Description

The PPM8P40V8 uses advanced trench technology to provide excellent RDS(on), low gate charge and operation with gate voltage as low as -4.5V. This device is suitable for use as a wide variety of application.

D (5、6、7、8)



MOSFET Product Summary		
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)	I <sub>D</sub> (A)
-40	21@ V <sub>GS</sub> =-10V	-8
	31@ V <sub>GS</sub> =-4.5V	

## Absolute Maximum Ratings @25°C

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V <sub>GS</sub> =0V)	V <sub>DS</sub>	-40	V
Gate-Source Voltage (V <sub>DS</sub> =0V)	V <sub>GS</sub>	±20	V
Drain Current-Continuous (TC=25°C)	I <sub>D</sub>	-8	A
Drain Current-Continuous (TC=100°C)		-5	A
Drain Current-Continuous @Current-Pulse (Note1)	I <sub>DM(pulse)</sub>	-32	A
Maximum Power Dissipation (TC=25°C)	P <sub>D</sub>	3	W
Maximum Power Dissipation (TC=100°C)		1.2	W
Operating Junction and storage Temperature Range	T <sub>J,T<sub>STG</sub></sub>	-55 To 150	°C

## Thermal Characteristic

Parameter	Symbol	Min.	Typ.	Max.	Units
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>		42		°C/W

## Electrical characteristics per line@25°C( unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
<b>On/Off States</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	-40			V
Zero Gate Voltage Drain Current	I <sub>bss</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V			-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.1	-1.7	-2.5	V
Forward Tran conductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-5A	15			S
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-8A		16	21	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-6A		21	31	mΩ

DYNAMIC PARAMETERS						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, V_{DS}=-20V,$ $f=1.0MHz$		2050		pF
Output Capacitance	$C_{DSS}$			260		pF
Reverse Transfer Capacitance	$C_{RSS}$			150		pF
SWITCHING TIMES						
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-20V, V_{GS}=-10V,$ $R_L=1.6\Omega, R_{GEN}=3\Omega$		10		ns
Turn-On Rise Time	$t_r$			24		ns
Turn-Off Delay Time	$t_{d(off)}$			40		ns
Turn-Off Fall Time	$t_f$			9		ns
Total Gate Charge	$Q_g$	$V_{DS}=-20V, V_{GS}=-10V,$ $I_D=-8A$		45		nC
Gate-Source Charge	$Q_{gs}$			6		nC
Gate-Drain Charge	$Q_{gd}$			11		nC
Source-Drain Diode Characteristics						
Source-Drain Current (Body Diode)	$I_{SD}$				-8	A
Forward on Voltage	$V_{SD}$	$V_{GS}=0V, I_S=-8A$			-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

## Typical Characteristics

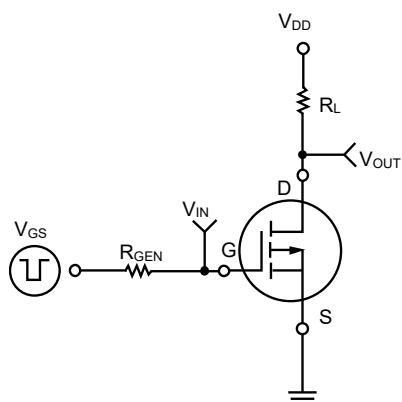


Figure 1. Power Dissipation

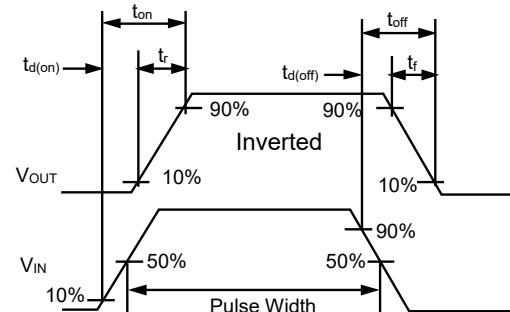


Figure 2. Drain Current

## P-Channel MOSFET

PPM8P40V8

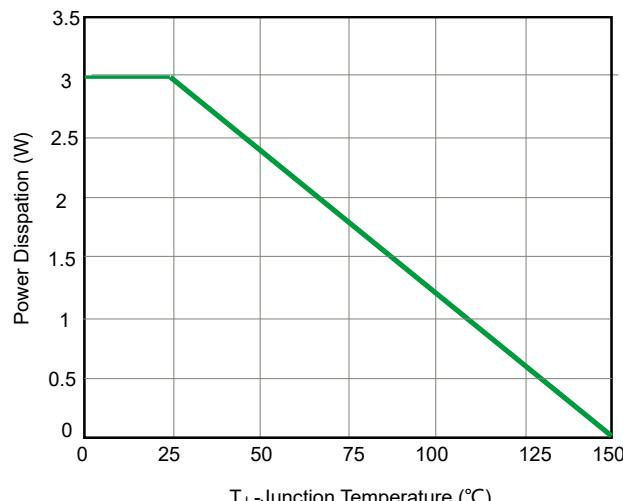


Fig 3. Output Characteristics

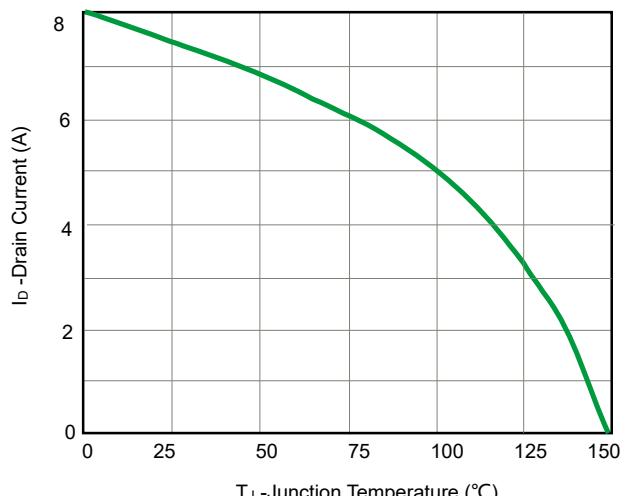


Fig 4. Transfer Characteristics

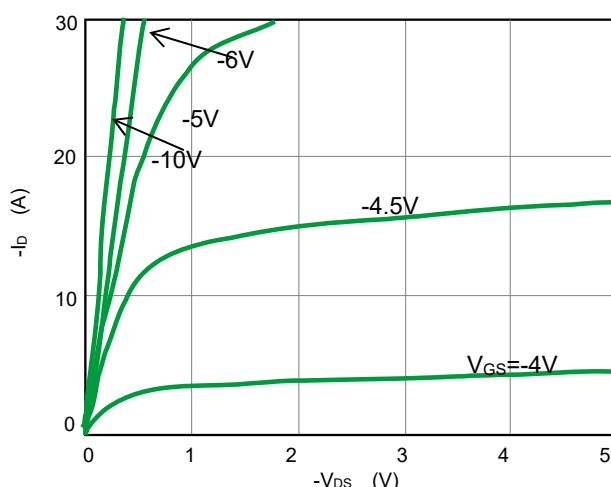


Fig 5. Capacitance

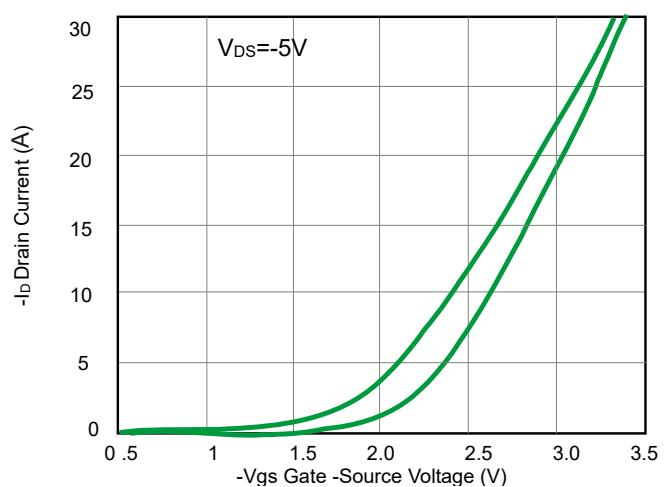


Fig 6.  $R_{DS(ON)}$  vs Junction Temperature

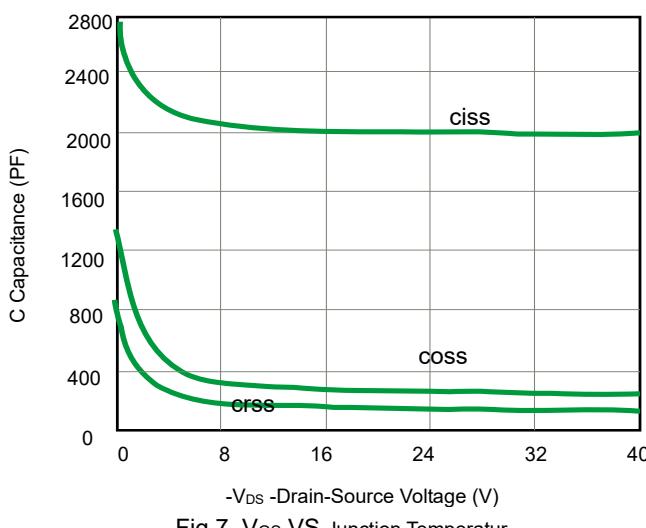


Fig 7.  $V_{GS}$  VS Junction Temperature

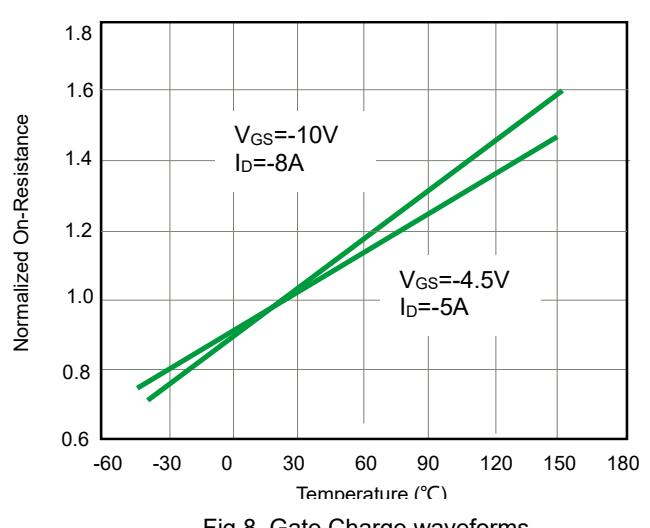


Fig 8. Gate Charge waveforms

## P-Channel MOSFET

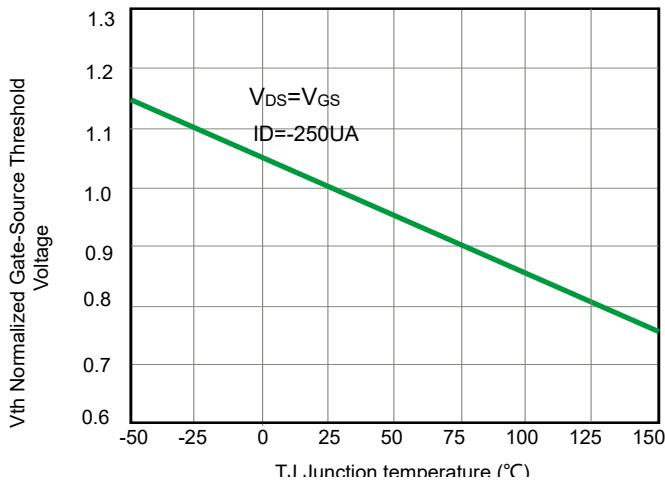


Fig 9. SOA

## PPM8P40V8

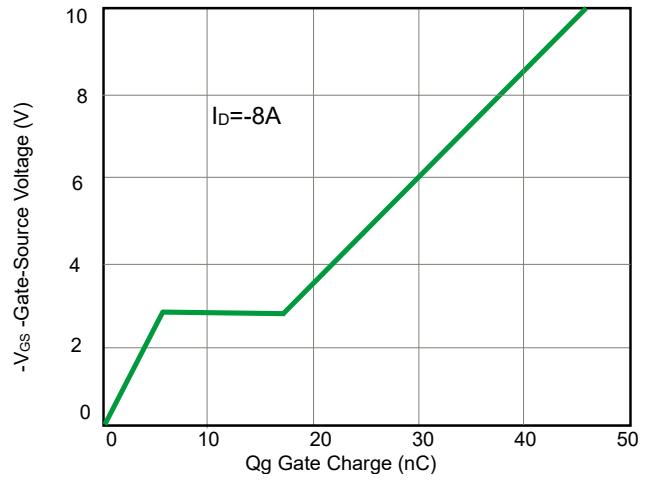


Fig 10. Gate Charge

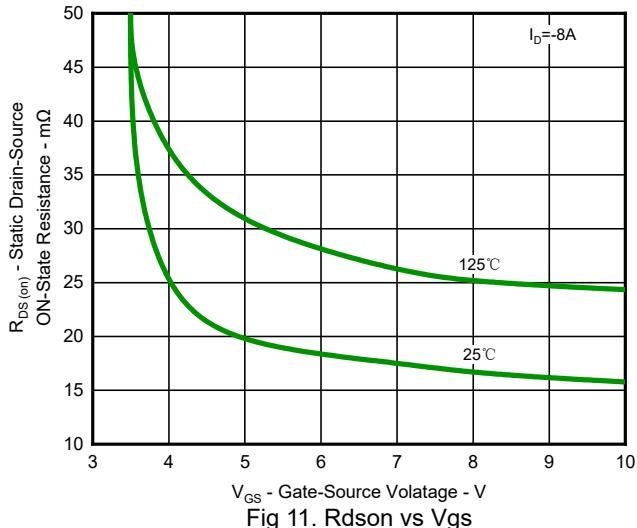


Fig 11.  $R_{DS(on)}$  vs  $V_{GS}$

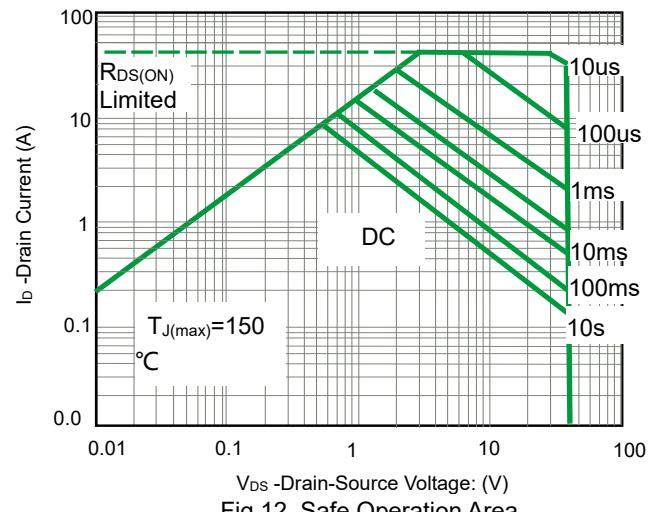


Fig 12. Safe Operation Area

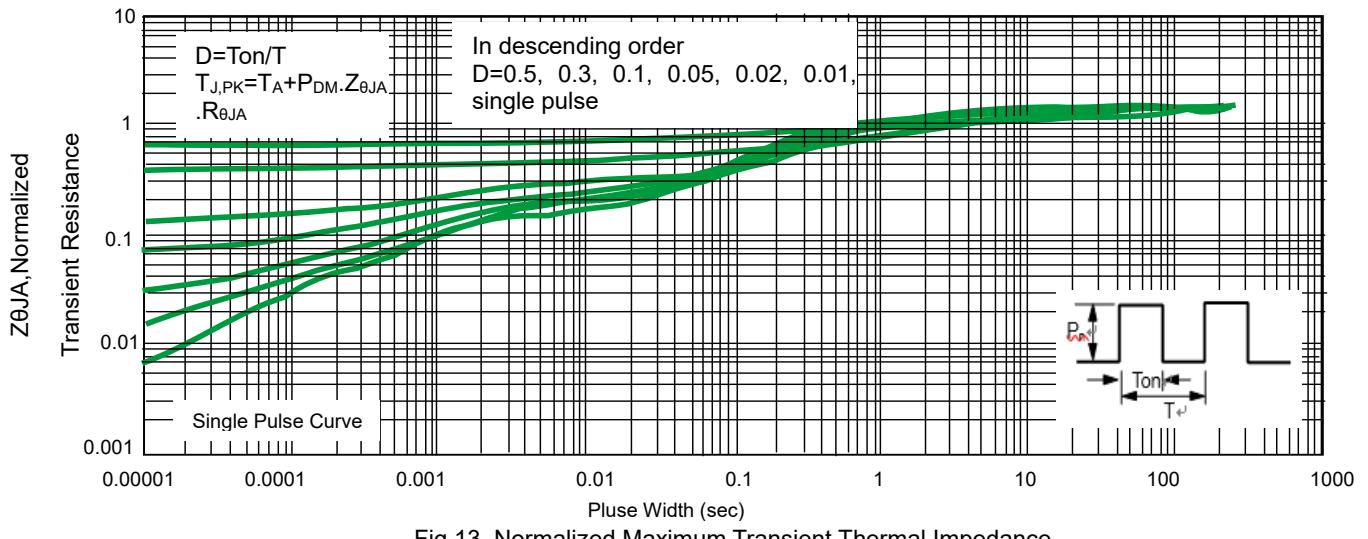
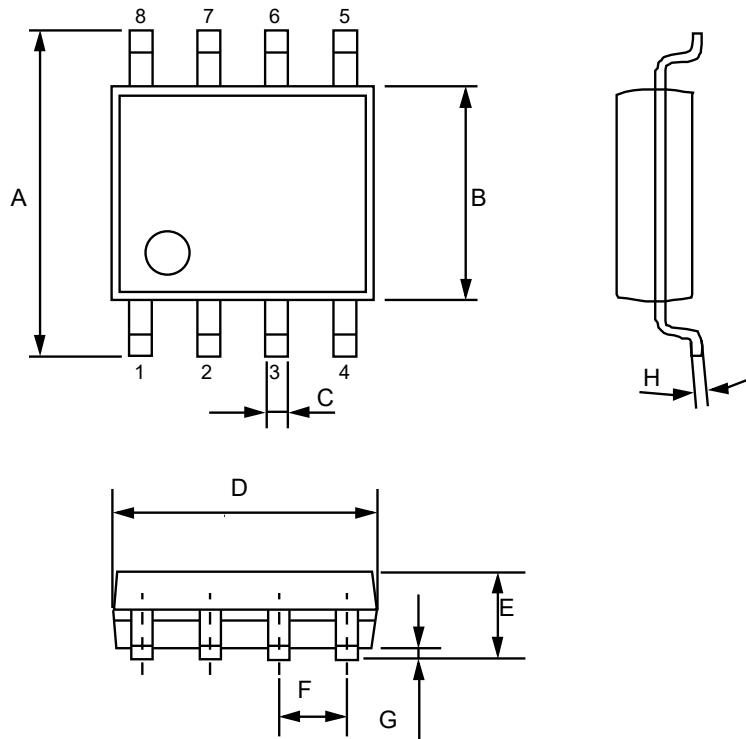


Fig 13. Normalized Maximum Transient Thermal Impedance

## Product dimension (SOP-8)

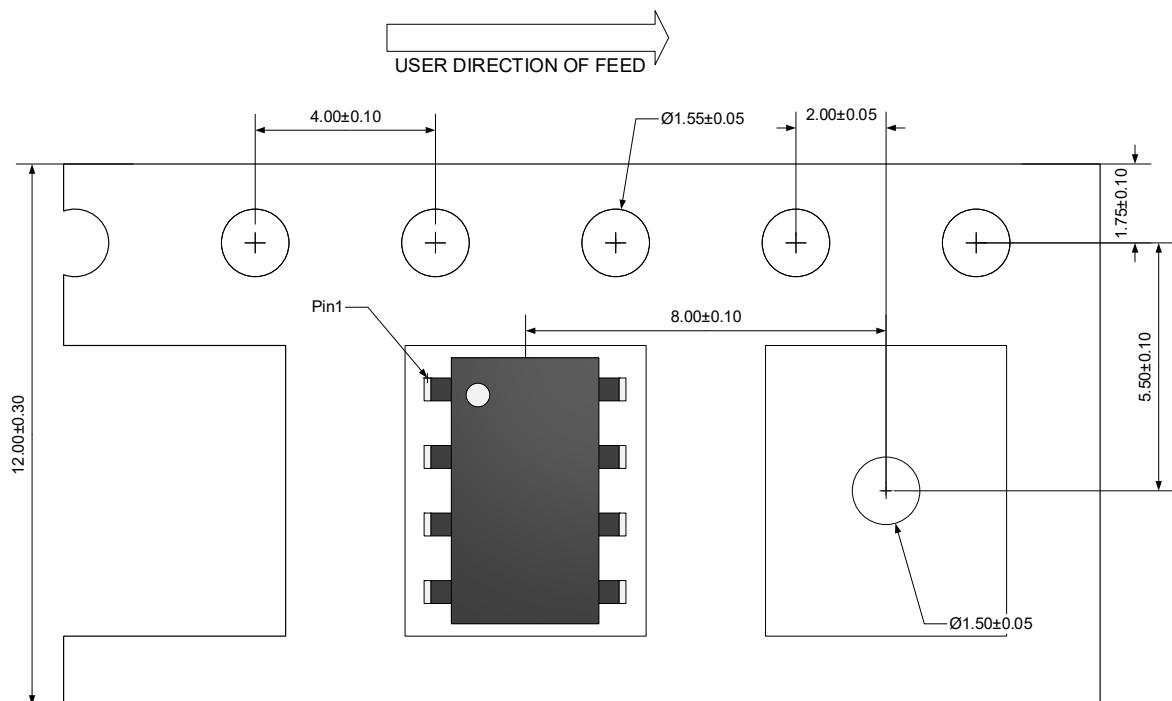


Dim	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	5.800	6.200	0.228	0.244
B	3.800	4.000	0.150	0.157
C	0.330	0.510	0.013	0.020
D	4.700	5.100	0.185	0.200
E	1.350	1.750	0.053	0.069
F	1.270 (BSC)		0.050 (BSC)	
G	0.100	0.250	0.004	0.010
H	0.170	0.250	0.006	0.010

## Ordering information

Device	Package	Reel	Shipping
PPM8P40V8	SOP-8	13"	4000 / Tape & Reel

## Load with information



Unit:mm

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