Prisemi®

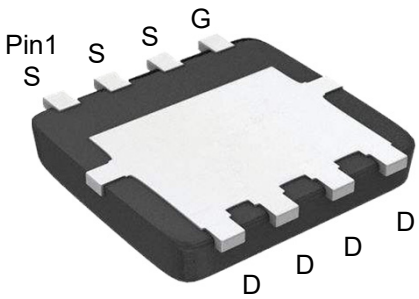
PSM8N10R4

N-Channel MOSFET

Description

The PSM8N10R4 uses split gate trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for power management and high efficiency applications at high switching frequencies applications.

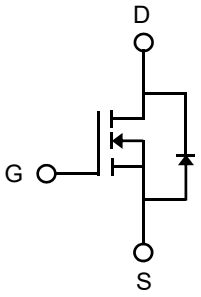
MOSFET Product Summary		
V_{DS}(V)	$R_{DS(on)}$(mΩ)(Typ)	I_D(A)
100	3.2@ V_{GS} = 10V	135



PDFN5060-8L
(Bottom View)

Feature

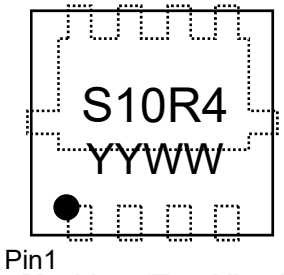
- Low $R_{DS(ON)}$ - Ensures On-State Losses are Minimized
- Excellent Q_{gd} x $R_{DS(ON)}$ Product(FOM)
- Advanced Technology for DC-DC Converts
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- 100% UIS (Avalanche) Rated
- Lead-Free Finish ; RoHS Compliant
- Halogen and Antimony Free. "Green" Device



Circuit Diagram

Applications

- PWM applications
- Load switch
- Power management
- DC-DC Converters
- Wireless Chargers



Marking (Top View)

Absolute maximum rating@25℃

Rating		Symbol	Value	Units
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Drain Current-Continuous[1]	T_C=25℃	I_D	135	A
	T_C=100℃		85	
Pulsed Drain Current[2]		I_{DM}	539	A
Total Power Dissipation[4]	T_C=25℃	P_D	125	W
	T_C=100℃		50	
Avalanche Current @ L=0.1mH		I_{AS}	72	A
Avalanche Energy @ L=0.1mH		E_{AS}	259	mJ
Thermal Resistance , Junction-to-Case[4]		$R_{\theta JC}$	1.0	℃/W
Thermal Resistance Junction-to-Ambient[3]		$R_{\theta JA}$	43	℃/W
Junction and Storage Temperature Range		T_J,T_{STG}	-55~+150	℃

Electrical characteristics per line@25℃ (unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Units
Off Characteristics							
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} = 0V,I_D = 250μA		100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}= 100V, V_{GS}= 0V	T_J=25℃	-	-	1.0	μA
			T_J=55℃	-	-	10	
Gate-Body Leakage Current	I_{GSS}	V_{GS} = ±20V,V_{DS} = 0V		-	-	±100	nA
On Characteristics[5)]							
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS},I_D = 250μA		2.0	3.0	4.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS} = 10V,I_D = 20A		-	3.2	3.8	mΩ
Forward Transconductance	g_{fs}	V_{DS} = 5 V,I_D = 20A		-	46	-	S
Diode Forward Voltage	V_{SD}	V_{GS} = 0V,I_S = 2A		-	0.7	1.2	V
Dynamic Characteristics[6)]							
Input Capacitance	C_{lss}	V_{DS} = 50V,V_{GS} = 0V, f = 1.0MHz		-	4799	-	pF
Output Capacitance	C_{oss}			-	1256	-	
Reverse Transfer Capacitance	C_{rss}			-	50	-	
Switching Characteristics[6)]							
Turn-on Delay Time	$t_{d(on)}$	V_{DS} = 50V, V_{GS} = 10V, R_{GEN} = 3Ω, I_D = 20A		-	10	-	ns
Turn-on Rise Time	t_r			-	17	-	
Turn-Off Delay Time	$t_{d(off)}$			-	44	-	
Turn-Off Fall Time	t_f			-	23	-	
Total Gate Charge	Q_g	V_{DS} = 50V, I_D = 20A, V_{GS} =0 to 10V		-	68	-	nC
Gate-Source Charge	Q_{gs}			-	19	-	
Gate-Drain Charge	Q_{gd}			-	15	-	
Gate Resistance	R_g	V_{GS}=0V,V_{DS}=0V,f=1MHz		-	1.7	-	Ω
Drain-Source Diode Characteristics[6)]							
Reverse Recovery Time	t_{rr}	I_F=20A, d_i/d_t=100A/μs		-	62	-	ns
Reverse Recovery Charge	Q_{rr}			-	130	-	nC
Diode Forward Current	I_S	-		-	-	135	A

Notes:
1. Pluse width limited by maximum junction temperature.
2. Pulse test : Pulse width ≤ 100μs, duty cycle ≤ 2%.
3. Device mounted on 1 inch FR4 PCB with 2oz.Copper.
4. Device mounted on infinite heatsink.
5. Measured under pulsed conditions. Pulse width ≤ 300μs, duty cycle ≤ 2%.
6. Guaranteed by design, not subject to production.

Typical Characteristics

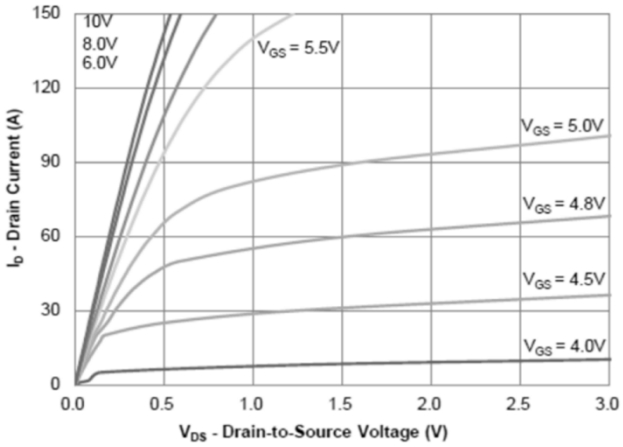


Figure 1: Output Characteristics

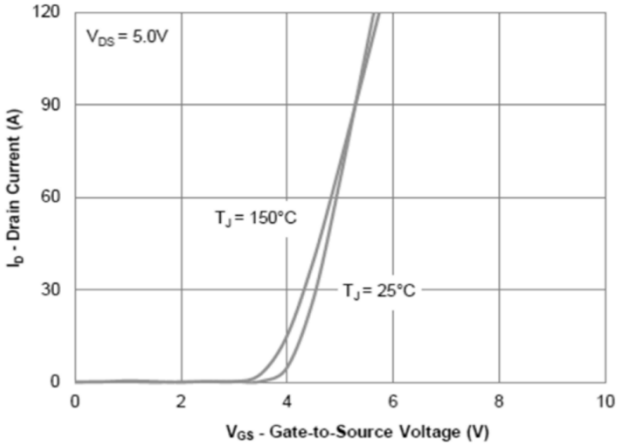


Figure 2: Transfer Characteristics

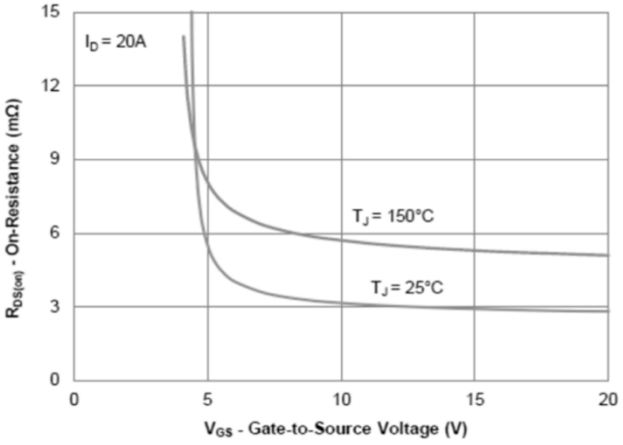


Figure 3: On-Resistance vs. Gate-Source Voltage

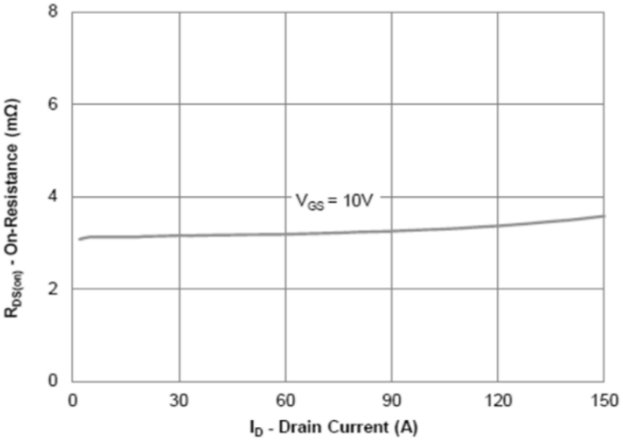


Figure 4: On-Resistance vs. Gate-Source Voltage

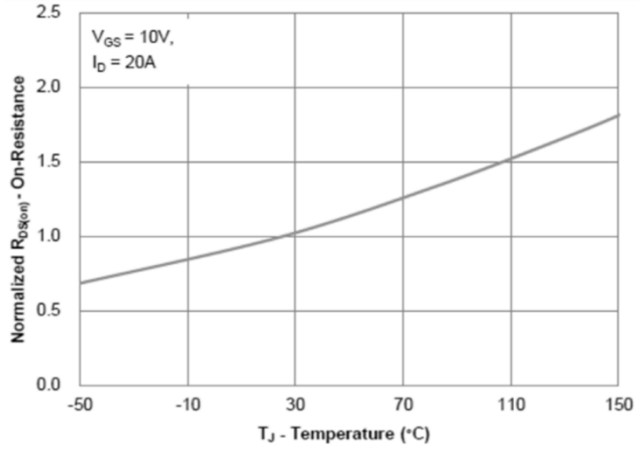


Figure 5: On-Resistance vs. Junction Temperature

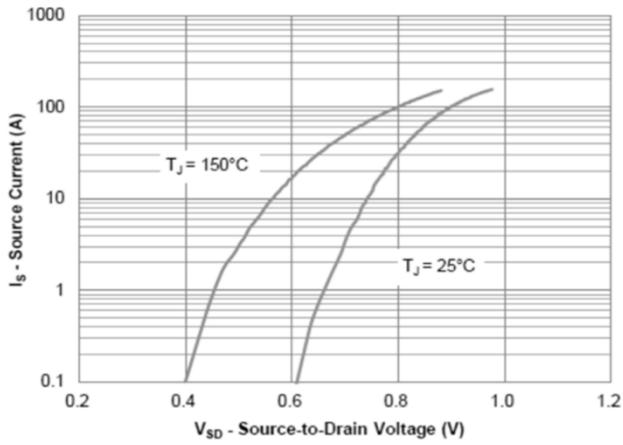


Figure 6: Source-Drain Diode Forward Voltage

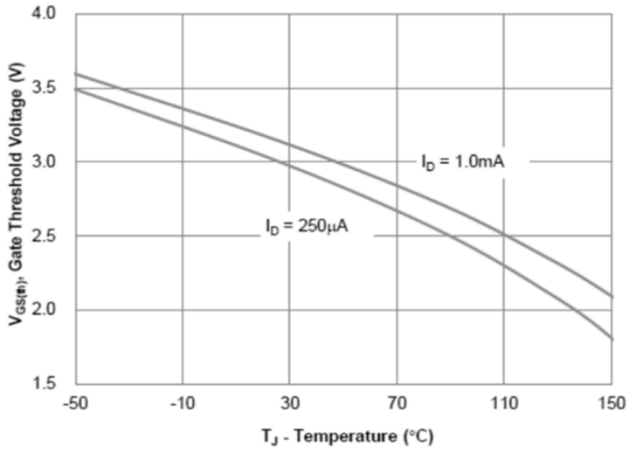


Figure 7: Gate Threshold Variation vs. Junction Temperature

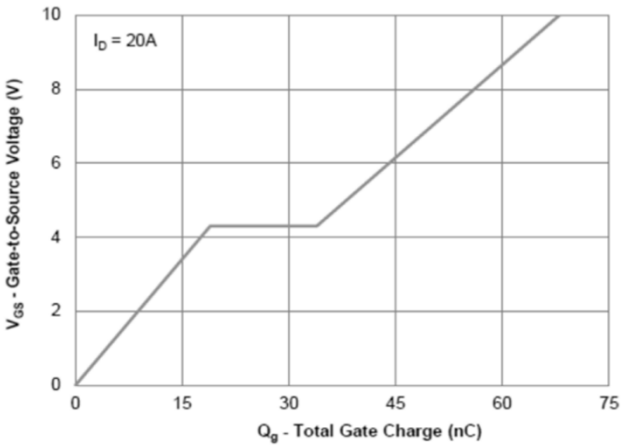


Figure 8: Gate Charge Characteristics

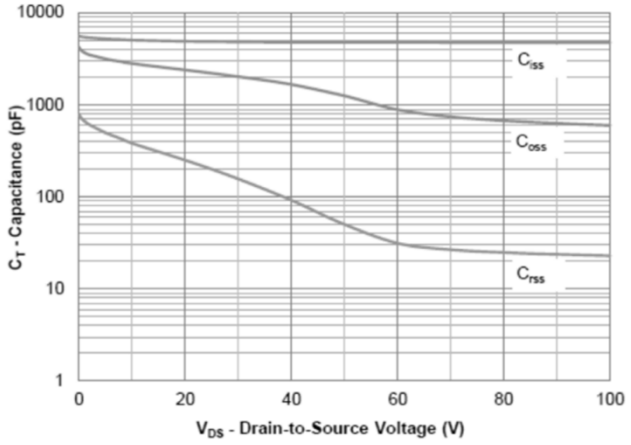


Figure 9: Capacitance Characteristics

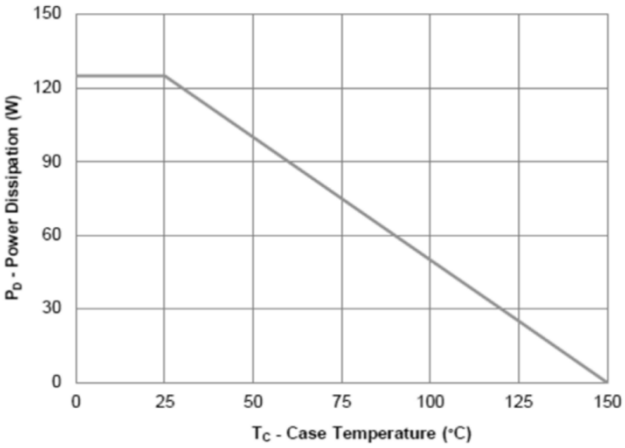


Figure 10: Power Derating

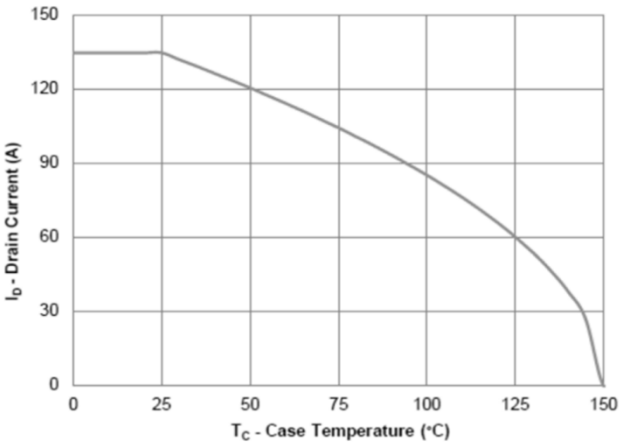


Figure 11: Current Derating

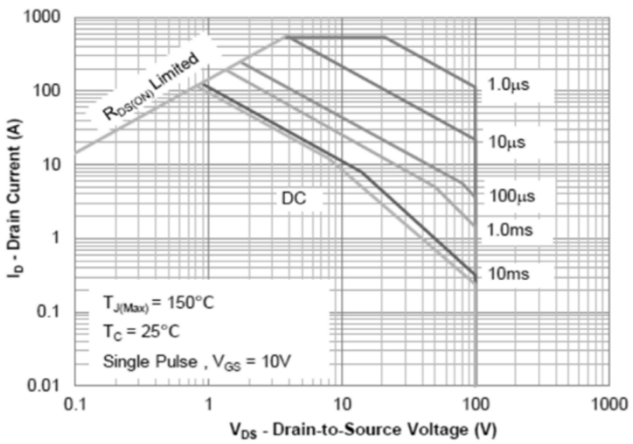


Figure 12: Safe Operating Area

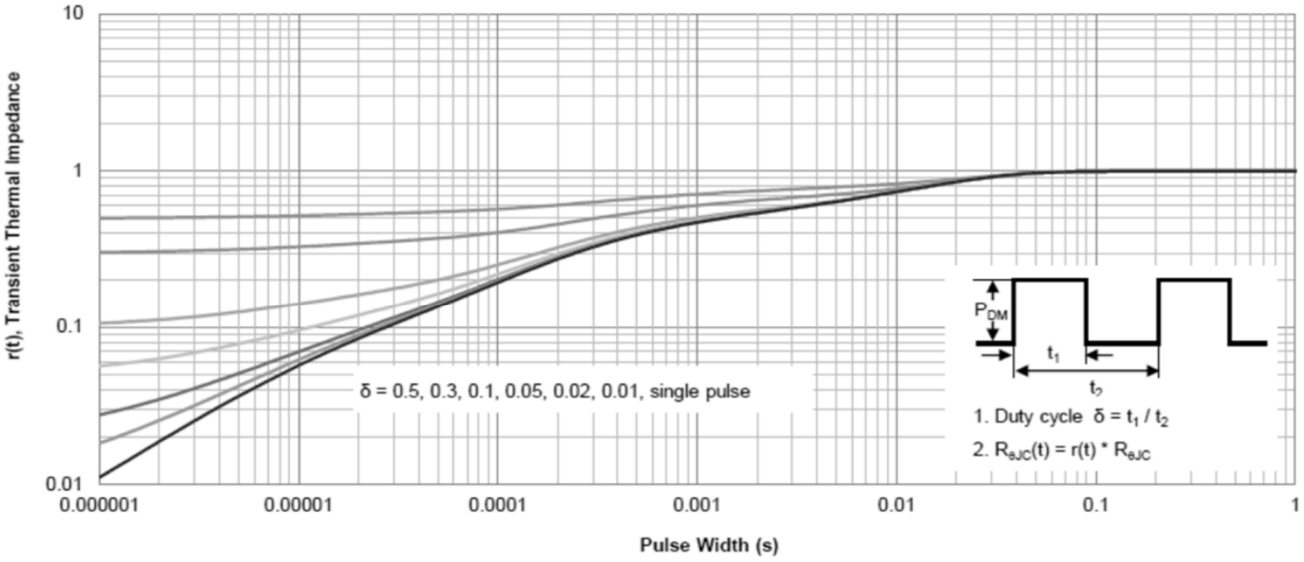
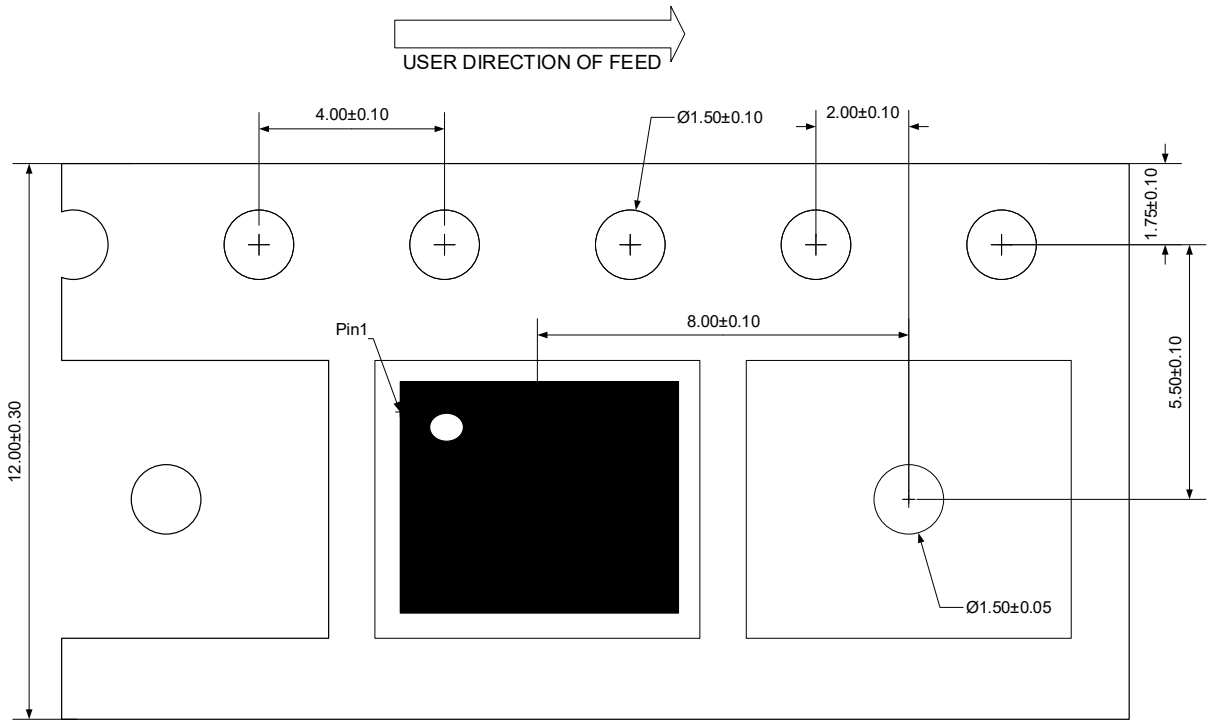


Figure 13: Normalized Maximum Transient Thermal Impedance

Ordering Information

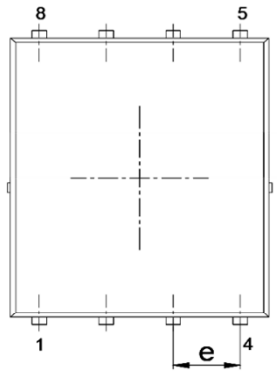
Device	Package	Reel	Shipping
PSM8N10R4	PDFN5060-8L	13"	5000 / Tape & Reel

Load With Information

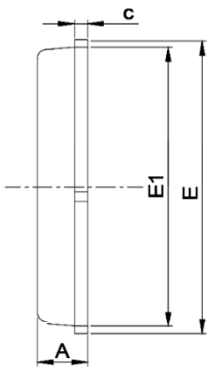


Unit:mm

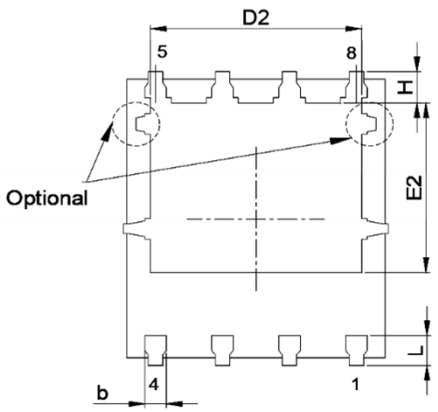
Product Dimension (PDFN5060-8L)



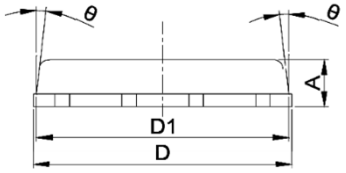
Top View



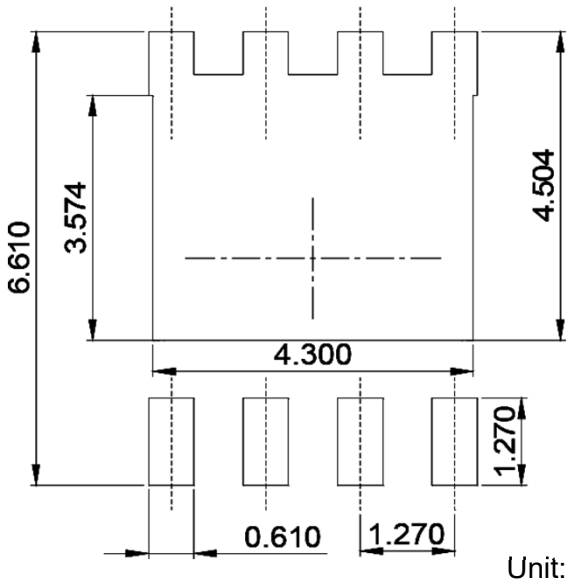
Side View



Bottom View



Front View



Unit: mm

Suggested PCB Layout

Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	0.90	1.10	0.035	0.043
b	0.20	0.51	0.008	0.020
c	0.21	0.34	0.008	0.013
D	4.90	5.40	0.193	0.213
D1	4.80	5.15	0.189	0.203
D2	3.91	4.20	0.154	0.165
E	5.90	6.50	0.232	0.256
E1	5.65	5.95	0.222	0.234
E2	3.32	3.63	0.131	0.143
e	1.27 BSC.		0.050 Ref.	
H	0.50	0.93	0.020	0.037
L	0.45	0.91	0.018	0.036
θ	0°	12°	0°	12°

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