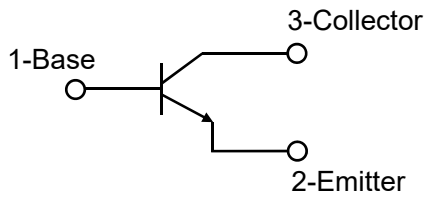
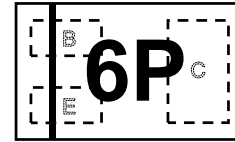


Feature

- This device is Pb-Free, Halogen Free/BFR Free and Rohs compliant.



Circuit Diagram



Marking (Top View)

Mechanical Characteristics

- DFN1006-3L without plating
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements

Absolute maximum rating@25°C

Parameter	Symbol	Value	Units
Collector-Base Voltage	V_{CBO}	60	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	6	V
Collector Current -Continuous	I_C	200	mA
Collector Dissipation	P_C	100 ^①	mW
		590 ^②	
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	1250 ^①	°C/W
		212 ^②	
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55 ~ +150	°C

Notes:

- ①.Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- ②.Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1cm².

Electrical characteristics per line@25°C

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = 10\mu A, I_E = 0$	60	-	-	V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = 1mA, I_B = 0$	40	-	-	V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = 10\mu A, I_C = 0$	6	-	-	V
Collector cut-off current	I_{CBO}	$V_{CB} = 60V, I_E = 0$	-	-	0.1	μA
Collector cut-off current	I_{CEX}	$V_{CE} = 30V, V_{BE(off)} = 3V$	-	-	50	nA
Emitter cut-off current	I_{EBO}	$V_{EB} = 5V, I_C = 0$	-	-	0.1	μA
DC current gain	h_{FE}	$V_{CE} = 1V, I_C = 10mA$	100	-	300	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 50mA, I_B = 5mA$	-	-	0.3	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C = 50mA, I_B = 5mA$	-	-	0.95	V
Transition frequency	f_T	$V_{CE} = 20V, I_C = 10mA, f = 100MHz$	300	-	-	MHz
Delay time	t_d	$V_{CC} = 3V, V_{BE(off)} = 0.5V, I_C = 10mA, I_{B1} = 1mA$	-	-	35	ns
Rise time	t_r		-	-	35	ns
Storage time	t_s	$V_{CC} = 3V, I_C = 10mA, I_{B1} = I_{B2} = 1mA$	-	-	200	ns
Fall time	t_f		-	-	50	ns

Typical Characteristics

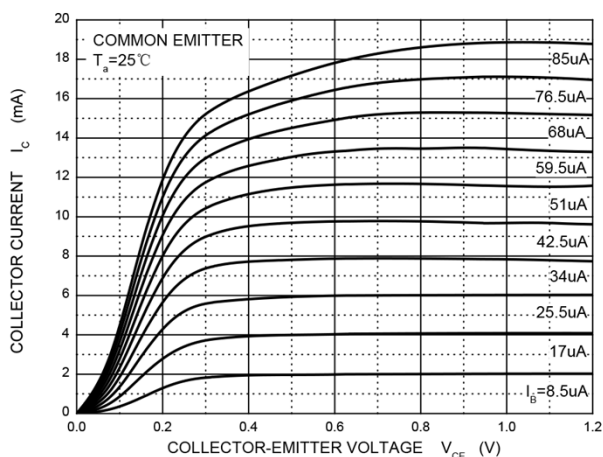


Fig 1. Static Characteristic

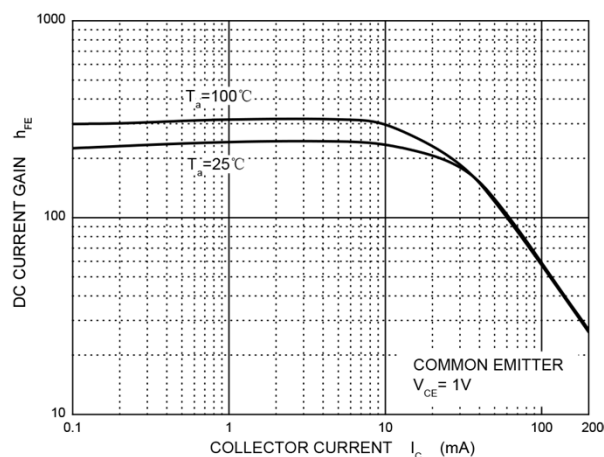


Fig 2. h_{FE} ----- I_C

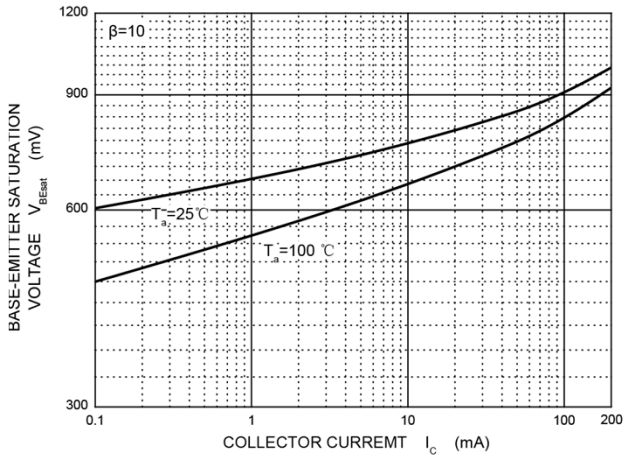


Fig 3. V_{BEsat} ----- I_C

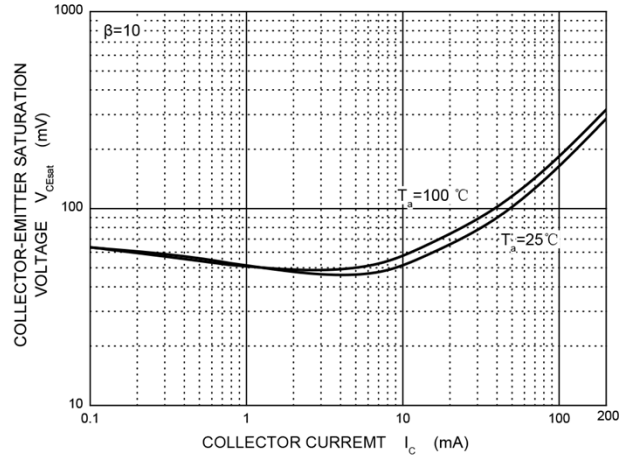


Fig 4. V_{CEsat} ----- I_C

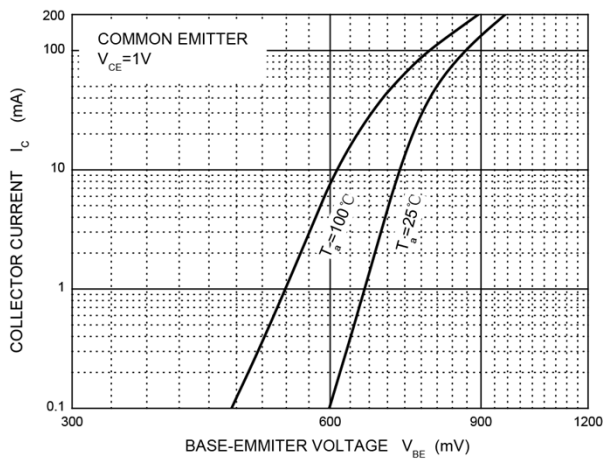


Fig 5. I_C ----- V_{BE}

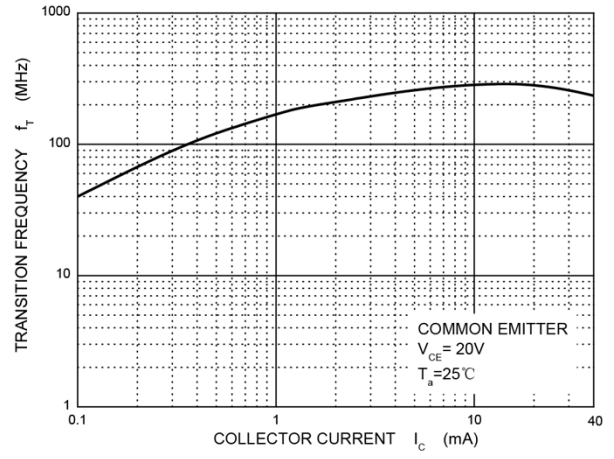


Fig 6. f_T ----- I_C

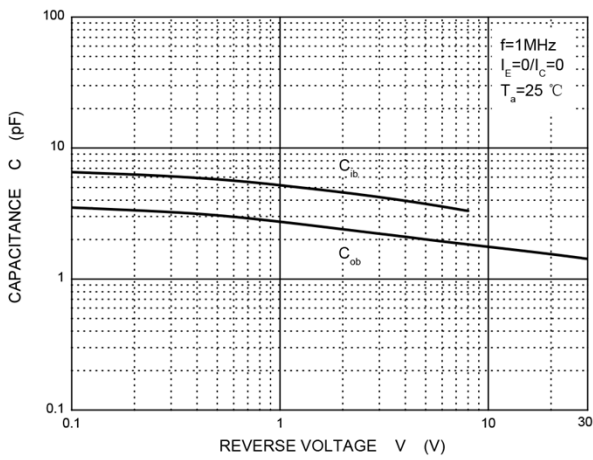


Fig 7. C_{ob}/C_{ib} ----- V_{CB}/V_{EB}

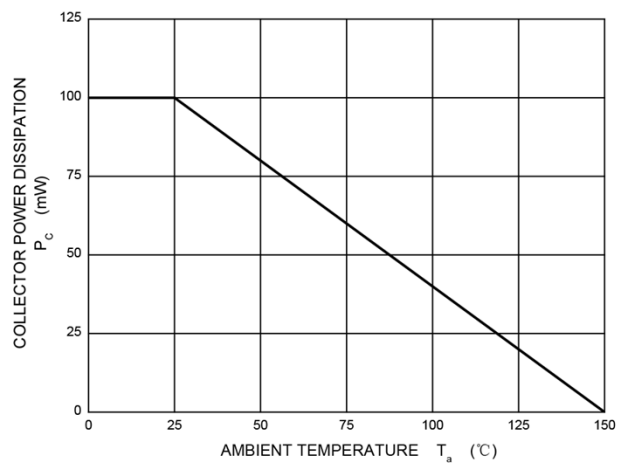
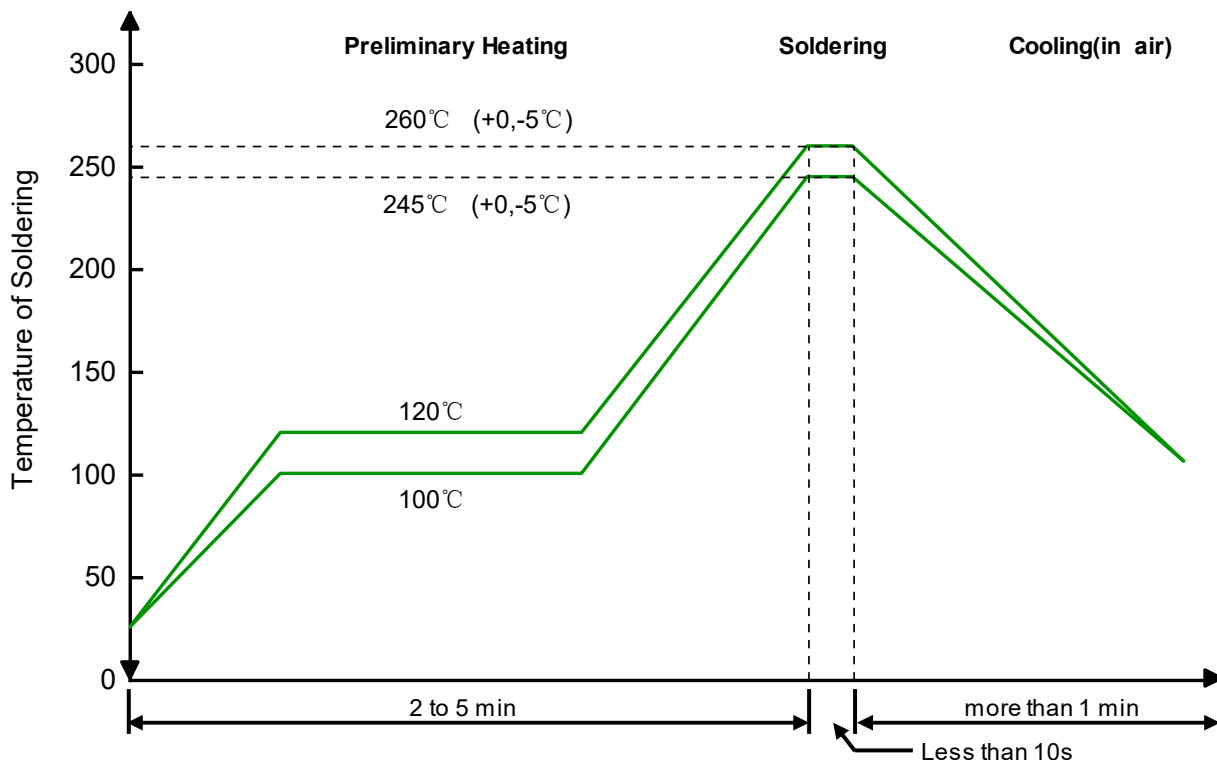


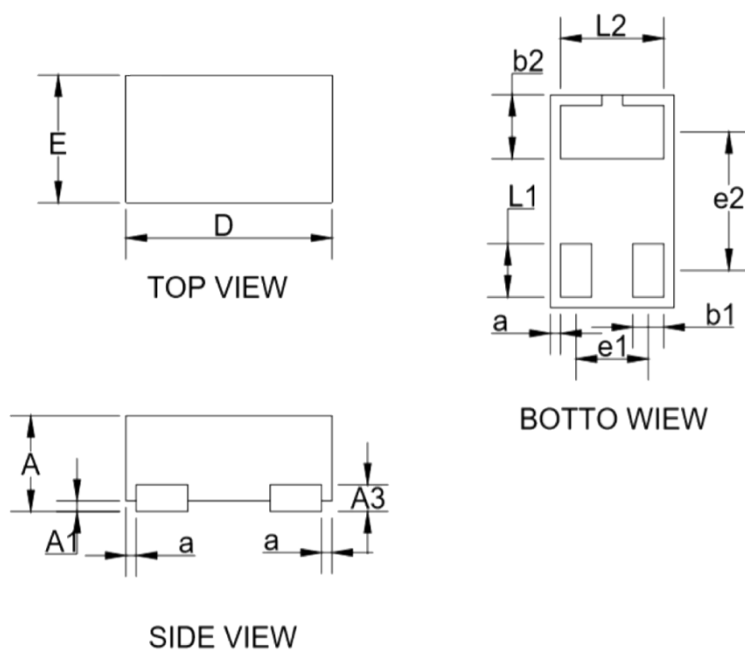
Fig 8. P_C ----- T_a

Solder Reflow Recommendation

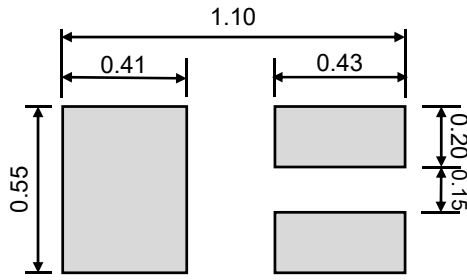


Remark: Pb free for 260°C; Pb for 245°C.

Product dimension (DFN1006-3L)



Dim	Millimeters		
	Min	Nom	Max
A	0.40	-	0.50
A1	0.00	-	0.05
A3	0.125 Ref.		
D	0.95	1.00	1.05
E	0.55	0.60	0.65
b1	0.10	0.15	0.20
b2	0.20	0.25	0.30
L1	0.20	0.25	0.30
L2	0.40	0.50	0.60
a	-	-	0.05
e1	0.35 BSC		
e2	0.65 BSC		



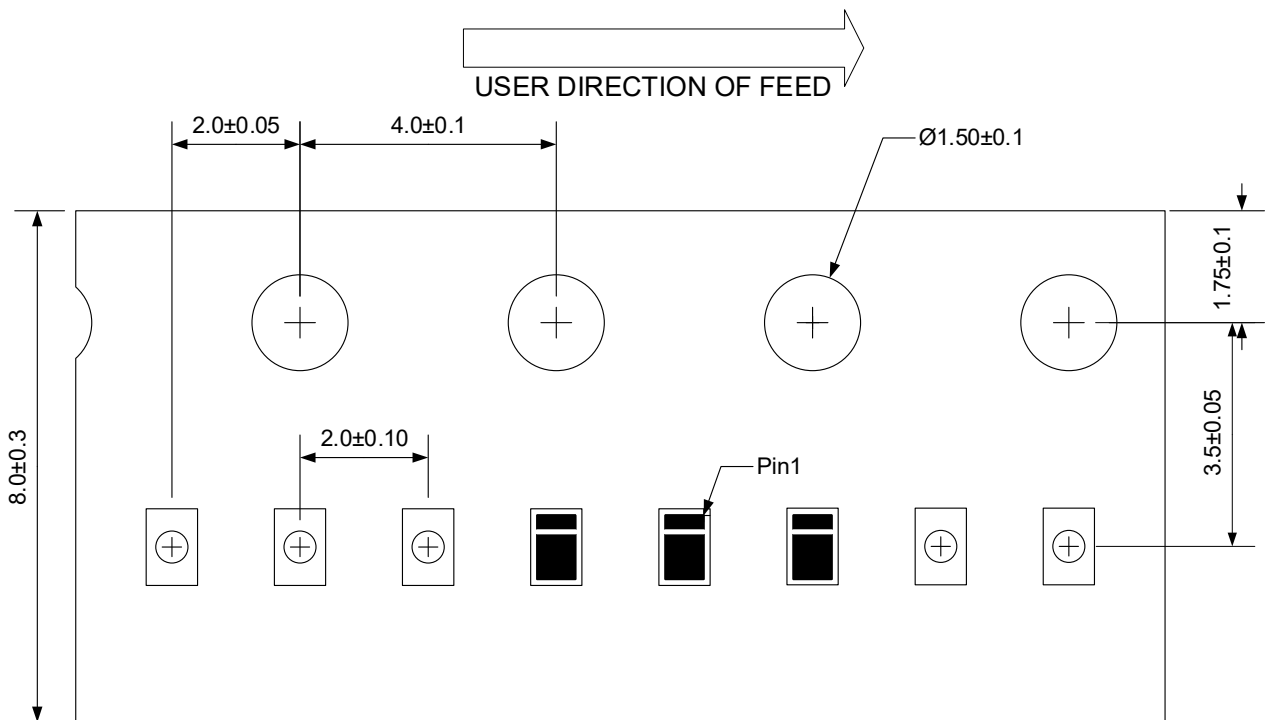
Suggested PCB Layout

Unit: mm

Ordering information


Device	Package	Reel	Shipping
PNT3FD3904	DFN1006-3L (Pb-Free)	7"	10000 / Tape & Reel

Load with information



Unit:mm


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